

## N-Channel Enhancement Mode Field Effect Transistor

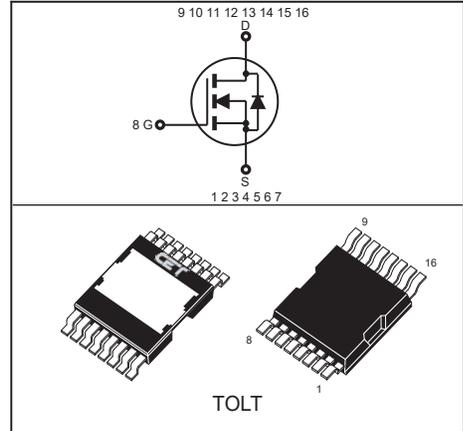
### FEATURES

- High power and current handling capability.
- Reliable and rugged.
- Pb-free lead plating ; RoHS compliant.
- Halogen Free.
- 100% Avalanche tested .

### APPLICATIONS

- Battery Management System.
- EV Charger.
- Energy Storage Systems.
- Motor control.

$V_{DS}$	$R_{DS(ON)}$ typ@ $V_{GS}$	$I_D$
100V	1.0m $\Omega$ @ $V_{GS} = 10V$	446A



### ABSOLUTE MAXIMUM RATINGS $T_C = 25^\circ\text{C}$ unless otherwise noted

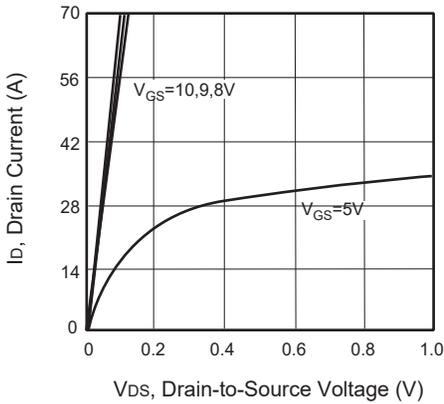
Parameter	Symbol	Limit	Units
Drain-Source Voltage	$V_{DS}$	100	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current-Continuous@ $T_C = 25^\circ\text{C}$ @ $T_C = 100^\circ\text{C}$	$I_D$	446	A
		315	A
Drain Current-Pulsed <sup>a</sup>	$I_{DM}$	1784	A
Maximum Power Dissipation @ $T_C = 25^\circ\text{C}$ - Derate above $25^\circ\text{C}$	$P_D$	517	W
		3.4	W/ $^\circ\text{C}$
Single Pulsed Avalanche Energy <sup>d</sup>	$E_{AS}$	1217	mJ
Single Pulsed Avalanche Current <sup>d</sup>	$I_{AS}$	78	A
Operating and Store Temperature Range	$T_J, T_{stg}$	-55 to 175	$^\circ\text{C}$

### Thermal Characteristics

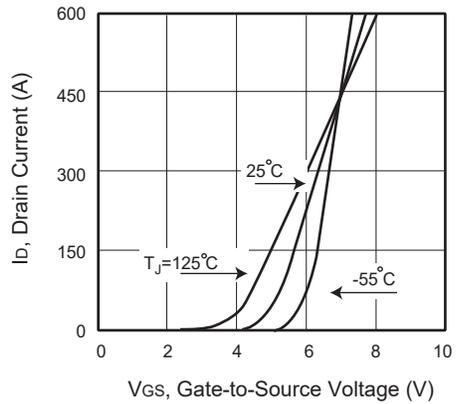
Parameter	Symbol	Limit	Units
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.29	$^\circ\text{C/W}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	60	$^\circ\text{C/W}$

## Electrical Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted

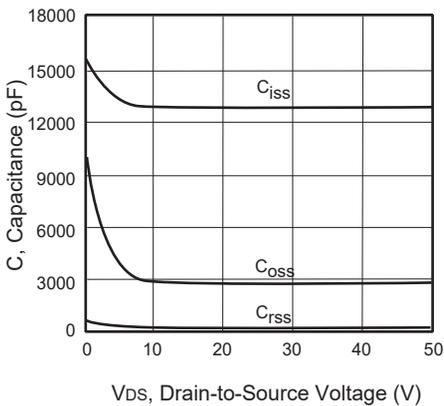
Parameter	Symbol	Test Condition	Min	Typ	Max	Units
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	100			V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 100V, V_{GS} = 0V$			1	$\mu A$
Gate Body Leakage Current, Forward	$I_{GSSF}$	$V_{GS} = 20V, V_{DS} = 0V$			100	nA
Gate Body Leakage Current, Reverse	$I_{GSSR}$	$V_{GS} = -20V, V_{DS} = 0V$			-100	nA
<b>On Characteristics<sup>b</sup></b>						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{GS} = V_{DS}, I_D = 250\mu A$	2.5		4.5	V
Static Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 50A$		1.0	1.3	$m\Omega$
Gate input resistance	$R_g$	$f=1\text{MHz, open Drain}$		1		$\Omega$
<b>Dynamic Characteristics<sup>c</sup></b>						
Input Capacitance	$C_{iss}$	$V_{DS} = 50V, V_{GS} = 0V,$ $f = 1.0 \text{ MHz}$		13100		pF
Output Capacitance	$C_{oss}$			2920		pF
Reverse Transfer Capacitance	$C_{rss}$			95		pF
<b>Switching Characteristics<sup>c</sup></b>						
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 50V, I_D = 20A,$ $V_{GS} = 10V, R_{GEN} = 6\Omega$		77		ns
Turn-On Rise Time	$t_r$			26		ns
Turn-Off Delay Time	$t_{d(off)}$			89		ns
Turn-Off Fall Time	$t_f$			46		ns
Total Gate Charge	$Q_g$	$V_{DS} = 50V, I_D = 20A,$ $V_{GS} = 10V$		140		nC
Gate-Source Charge	$Q_{gs}$			46		nC
Gate-Drain Charge	$Q_{gd}$			49		nC
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
Drain-Source Diode Forward Current	$I_S$				430	A
Drain-Source Diode Forward Voltage <sup>b</sup>	$V_{SD}$	$V_{GS} = 0V, I_S = 20A$			1.2	V
Reverse Recovery Time	$T_{rr}$	$I_F = 50A, di/dt = 100A/\mu s$		120		ns
Reverse Recovery Charge	$Q_{rr}$			324		nC
<b>Notes :</b> a.Repetitive Rating : Pulse width limited by maximum junction temperature b.Pulse Test : Pulse Width $\leq 300\mu s$ , Duty Cycle $\leq 2\%$ . c.Guaranteed by design, not subject to production testing. d.L = 0.4mH, $I_{AS} = 76A, V_{DD} = 60V, R_G = 25\Omega$ , Starting $T_J = 25^\circ\text{C}$ .						



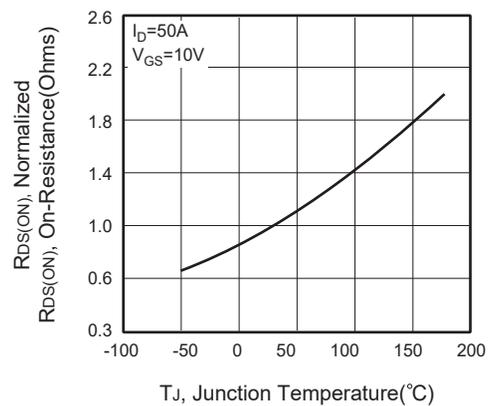
**Figure 1. Output Characteristics**



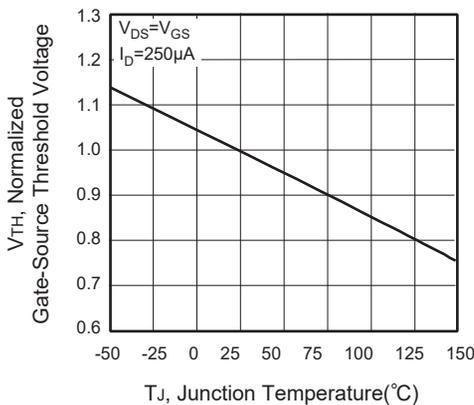
**Figure 2. Transfer Characteristics**



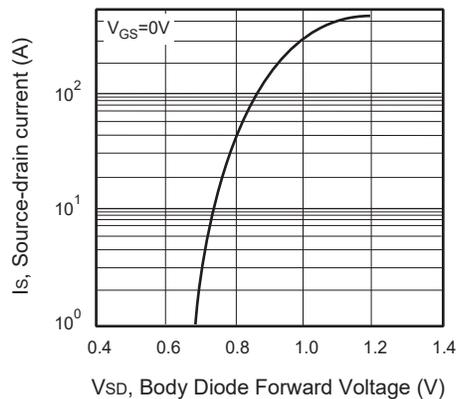
**Figure 3. Capacitance**



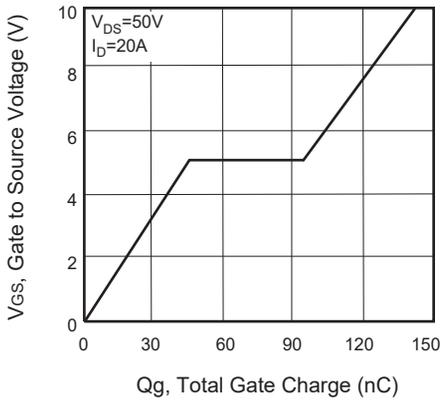
**Figure 4. On-Resistance Variation with Temperature**



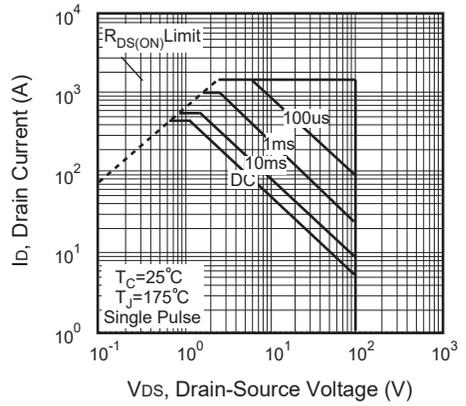
**Figure 5. Gate Threshold Variation with Temperature**



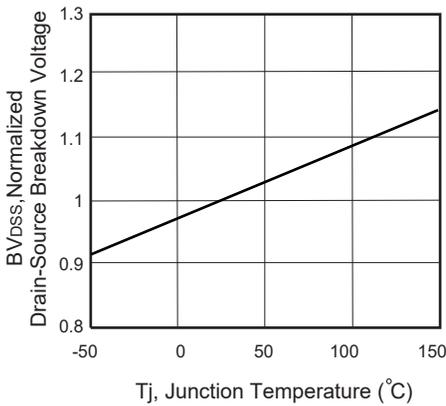
**Figure 6. Body Diode Forward Voltage Variation with Source Current**



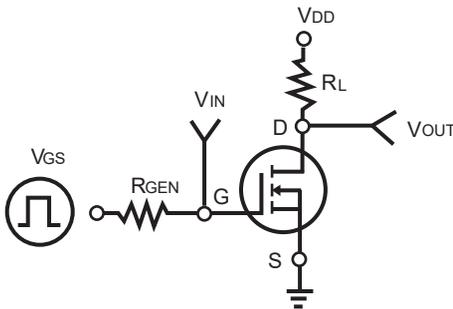
**Figure 7. Gate Charge**



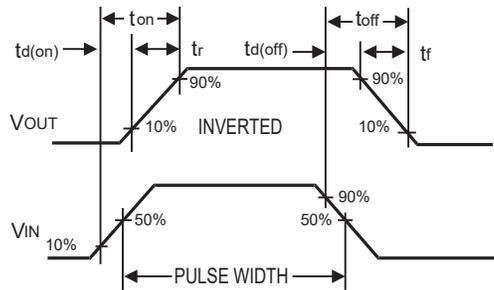
**Figure 8. Maximum Safe Operating Area**



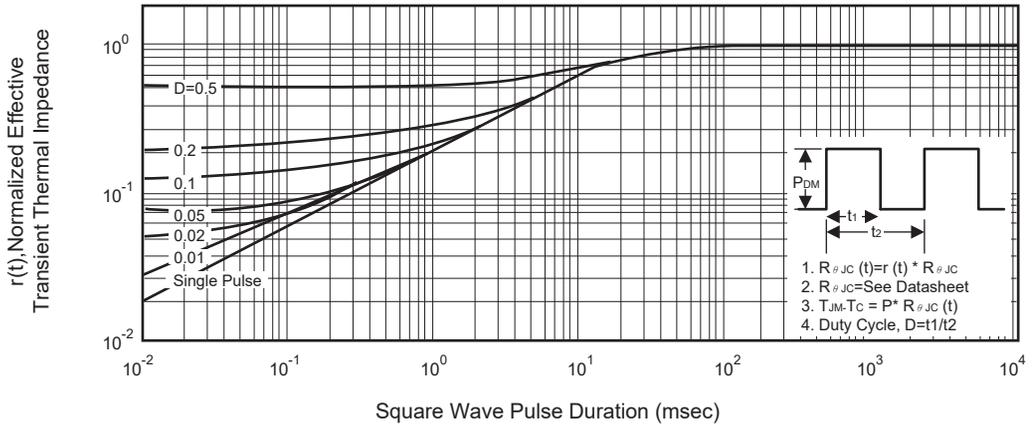
**Figure 9. Breakdown Voltage Variation VS Temperature**



**Figure 10. Switching Test Circuit**

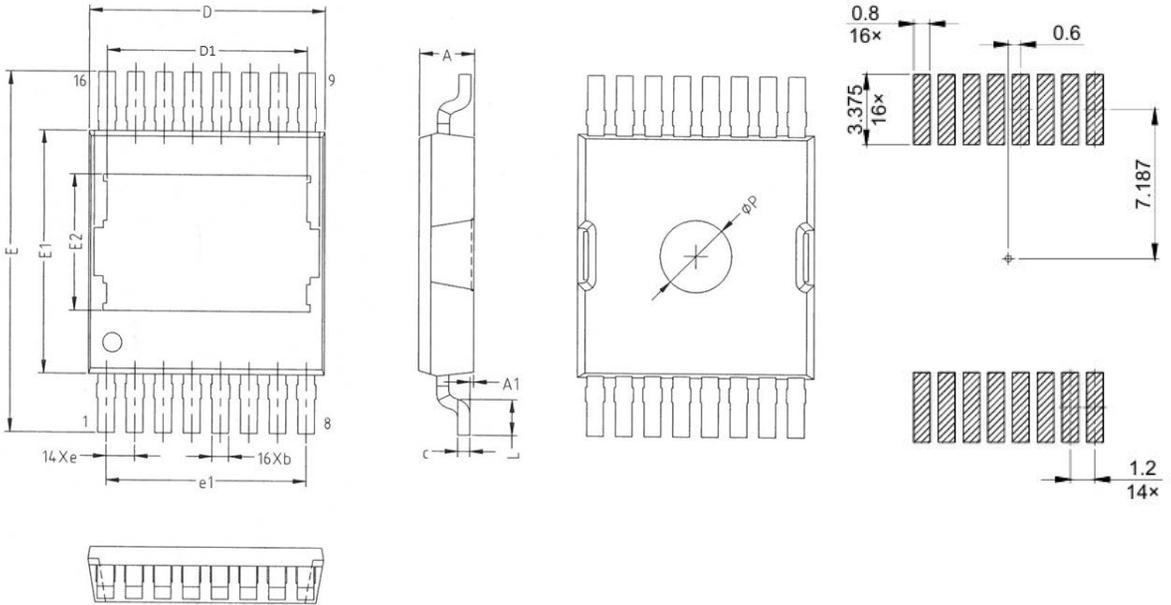


**Figure 11. Switching Waveforms**



**Figure 12. Normalized Thermal Transient Impedance Curve**

## TOLT 產品外觀尺寸圖 (Product Outline Dimension)



SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	2.250	2.350	0.089	0.093
A1	0.010	0.160	0.000	0.006
b	0.600	0.800	0.024	0.031
c	0.400	0.600	0.016	0.024
D	9.700	10.100	0.382	0.398
D1	8.300	8.500	0.327	0.335
E	14.800	15.200	0.583	0.598
E1	10.000	10.300	0.394	0.406
E2	5.570	5.770	0.219	0.227
e	1.20BSC		0.047BSC	
e1	8.40BSC		0.331BSC	
L	1.400	1.600	0.055	0.063
P	2.900	3.100	0.114	0.122